

6.2 TIMER CPU

2. IC1 (UPD75217CW-040)

NO	SYMBOL	LABEL	IN/OUT	NOTE
1	S3	Sd	OUT	KEY SCAN PULSE OUTPUT
2	S2	Sc	OUT	
3	S1	Sb	OUT	
4	S0	Sa	OUT	
5	P00/INT4	POWER DOWN	IN	POWER DOWN DETECT:L
6	P01/SCK	OS/DISP CLOCK	OUT	ON SCREEN/DISPLAY DATA TRANSFER CLOCK
7	P02/S0	OS/DISP DATA	OUT	ON SCREEN/DISPLAY SERIAL DATA
8	P03/S1	TEST	IN	TEST POINT(CLOCK ADJUST/FDP CHECK)
9	P10/INT0	REMOTE	IN	16BIT REMOTE DATA(A/B CODE)
10	P11/INT1	CATV CTL	IN	NOT USED
11	P12/INT2	CTL PULSE	IN	LINEAR TIME COUNTER DATA
12	P13/T10	CTL PULSE	IN	LINEAR TIME COUNTER DATA
13	P20	KS 0	IN	KEY SCAN DATA INPUT
14	P21	KS 1	IN	
15	P22	KS 2	IN	
16	P23/BUZ	KS 3	IN	
17	P30	MECHACON DATA	IN/OUT	MECHACON/TIMER SERIAL DATA
18	P31	MECHACON CLOCK	OUT	MECHACON/TIMER DATA TRANSFER CLOCK
19	P32	SERIAL DATA	IN/OUT	VIDEO PROGRAM SYSTEM SERIAL DATA: I ² C BUS
20	P33	SERIAL CLOCK	OUT	VIDEO PROGRAM SYSTEM DATA TRANSFER CLOCK
21	P60	OS CS	OUT	ON SCREEN IC CHIP SELECT:L
22	P61	OS RESET	OUT	ON SCREEN IC RESET:L
23	P62	DISP CE	OUT	DISPLAY IC CHIP ENABLE:L
24	P63	AUDIO INT	OUT	AUDIO CONTROL IC CHIP SELECT:L
25	P40	LED CLOCK	OUT	LED SERIAL DATA TRANSFER CLOCK
26	P41	LED SERIAL DATA	OUT	LED SERIAL DATA
27	P42	AUDIO CLOCK	OUT	AUDIO CONTROL SERIAL DATA TRANSFER CLOCK
28	P43	PROGRAM	OUT	PROGRAM BLUE BACK MODE:H
29	PPO	NC	-	NC
30	X1	X1	IN	MAIN SYSTEM CLOCK(4.19MHz)
31	X2	X2	-	
32	Vss	Vss	-	GND
33	XT1	XT1	IN	GND
34	XT2	XT2	-	NC
35	P50	JS/B	IN	JOG DIAL PULSE INPUT
36	P51	SHUTTLE	IN	SHUTTLE SW(PLAY/PAUSE:L, SEARCH:H)
37	P52	JS/A	IN	JOG DIAL PULSE INPUT
38	P53	AUDIO DATA	OUT	AUDIO CONTROL SERIAL DATA
39	RESET	RESET	IN	RESET AT CONNECT VCR TO AC
40	T0	NC	-	NC
41	T1	NC	-	
42	T2	NC	-	
43	T3	NC	-	
44	T4	NC	-	
45	T5	NC	-	
46	T6	NC	-	
47	T7	NC	-	
48	T8	NC	-	
49	T9	NC	-	
50	T10/S15/PH3	Sp	OUT	KEY SCAN PULSE OUTPUT
51	T11/S14/PH2	So	OUT	
52	T12/S13/PH1	Sn	OUT	
53	T13/S12/PH0	Sm	OUT	
54	T14/S11	Sl	OUT	
55	T15/S10	Sk	OUT	
56	V LOAD	V LOAD	IN	GND
57	V PRE	V PRE	IN	GND
58	S9	Sj	OUT	KEY SCAN PULSE OUTPUT
59	S8	Si	OUT	
60	S7	Sh	OUT	
61	S6	Sg	OUT	
62	S5	Sf	OUT	
63	S4	Se	OUT	
64	VDD	VDD	-	5V FOR THE SYSTEM CONTROL

Table 6-2 IC1 pin function